



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 1484

SERIAL NUMBER 10/805,822	FILING DATE 03/22/2004  RULE	CLASS 257	GROUP ART UNIT 2815	ATTORNEY DOCKET NO. 44471/298721
-----------------------------	---------------------------------------	--------------	------------------------	--

## APPLICANTS

Makoto Yamamoto, Saitama-ken, JAPAN;

Akio Iwabuchi, Saitama-ken, JAPAN;

*EOI*  
 \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 10/014,949 10/26/2001 PAT 6,737,722

*EOI*  
 \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN P2001-128187 04/25/2001

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 05/31/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	<i>EOI</i> Examiner's Signature	<i>EOI</i> Initials		

## ADDRESS

23370  
 JOHN S. PRATT, ESQ  
 KILPATRICK STOCKTON, LLP  
 1100 PEACHTREE STREET  
 ATLANTA, GA  
 30309

## TITLE

Lateral transistor having graded base region, semiconductor integrated circuit and fabrication method thereof

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees ( Filing )
		<input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )